

## Electronic Acknowledgement Receipt

EFS ID:	1325522		
Application Number:	10630516		
International Application Number:			
Confirmation Number:	7900		
Title of Invention:	Tiered built-in self-test (BIST) architecture for testing distributed memory modules		
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /zip	Pages (if appl.)
1		030192_amend_112006.pdf	118036	yes	7
<b>Multipart Description/PDF files in .zip description</b>					
<b>Document Description</b>		<b>Start</b>		<b>End</b>	
Amendment - After Non-Final Rejection		1		1	
Claims		2		6	
Applicant Arguments/Remarks Made in an Amendment		7		7	

**Warnings:**

**Information:**

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**New Applications Under 35 U.S.C. 111**

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.